REMARKS

Claims 1-36 remain pending in the current Application. Claims 1-3, 5-13, 15, 17, 20, 25, 30, and 31 have been amended. Applicant submits that the amendments do not add new matter to the current Application. All the amendments herein have been made in order to clarify the claims and not for prior art reasons. Applicant also submits that (1) no amendment made was related to the statutory requirements of patentability unless expressly stated herein, and (2) no amendment made was for the purpose of narrowing the scope of any claim, unless Applicant has argued herein that such amendment was made to distinguish over a particular reference or combination of references.

Applicant has amended each of the independent claims as discussed with the Examiner in a telephone conversation made by the Examiner to Applicant on May 24, 2006, in order to further clarify the language of the claims. For example, Applicant has clarified that the processor circuitry conditionally loads or stores, and that a next portion of the data stream initiated by the first instruction is loaded or stored based on some aspect of the second instruction. Also, some of the dependent claims have been amended in order to maintain consistency with the independent claims. Again, Applicant wishes to point the Examiner to pages 52-56 of the current Specification which provides description which may be helpful in understanding the pending claims.

In the telephone conversation, the Examiner suggested that the Applicant make these changes by filing a Supplemental Amendment; therefore, Applicant is submitting this Supplemental Amendment with the above amendments based on claims as amended in the Amendment mailed May 11, 2006. Applicant believes to have addressed the Examiner's points, and therefore respectfully solicits allowance of the pending claims. Contact me if there are any issues regarding this communication or the current Application.
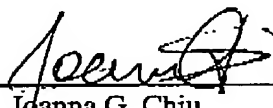
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If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.

Respectfully submitted,

SEND CORRESPONDENCE TO:

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